

Course: ECE 609 Semiconductor Devices
Instructor: Zlatan Aksamija (zlatana@engin.umass.edu)
Meeting Times: Tuesday and Thursday 11:30-12:45 am in ELabs RM 305
Office Hours: 201B Marcus Hall, Tuesday and Thursday 2-3 pm

Course Description (based on SPIRE): In-depth examination of semiconductor devices. The physics of semiconductors, p-n junction diodes, bipolar transistors, Schottky barriers, JFETs, and MOSFETs. Beyond CMOS devices will be explored through additional readings and term paper.

Prerequisite: E&C-Eng 344, or introductory semiconductor theory course.

Course Vision: this course serves two purposes: 1) as a graduate-level course in semiconductor devices for students interested in further study of this topic (aka the specialists), and 2) as a core PhD course in the semiconductor area for a broad array of students in the doctoral program (the generalists). As such we have the two-fold goal of depth and breadth—depth for the sake of the first group (the specialists) and breadth for the sake of the second group (the generalists). For these reasons, we will cover semiconductor materials and the underlying physics in some depth (Chapter 1), and then we will launch into a fairly broad survey of semiconductor devices, starting with junctions (Chapters 3-4), bipolar transistors (Chapters 6-7), and metal-oxide-semiconductor transistors (MOSFETs in Chapters 8-10). Because of the breadth requirement, we will not be able to cover all devices (the list is very long!!!) so we will have a series of reading assignments to complement the lectures, culminating in a term paper on a beyond-CMOS devices of each student's choice.

Course Goals:

- To understand the physical structure and electrical properties of semiconductor materials.
- To master the fundamental concepts and equations of semiconductor device analysis, and apply them to the description of semiconductor junctions and structures.
- To understand how the terminal characteristics of junction diodes, bipolar transistors, and field-effect transistors derive from device structure and material properties.

Required Textbook: Device Electronics for Integrated Circuits by Muller, Kamins, and Chan
http://www.amazon.com/Device-Electronics-Integrated-Circuits-Richard/dp/0471593982/ref=sr_1_32?ie=UTF8&qid=1452102821&sr=8-32&keywords=semiconductor+devices

Suggested (supplemental) Textbook: Advanced Semiconductor Fundamentals by Robert F. Pierret, Amazon: https://www.amazon.com/Advanced-Semiconductor-Fundamentals-Robert-Pierret/dp/013061792X/ref=sr_1_1?ie=UTF8&qid=1485266898&sr=8-1&keywords=pierret+advanced+semiconductor

Webpage: the course uses Moodle (<http://moodle.umass.edu>) for notes, homework handouts and solutions, announcements, and discussions. Lecture slides and/or scanned handwritten notes of the lectures will be uploaded after each section of the course. Please use the discussion forum in Moodle to ask questions relating to course content and homework.

Video Recordings: lectures are being recorded using UMass ECHO360 software. Lecture videos containing PPT slide, blackboard video and lecture audio will be posted via a link in Moodle; however, this process will not be immediate. There may be a several day delay between recording and the lecture video being ready to post; in addition, not all lectures are guaranteed to be made available due to unforeseen technical issues with ECHO360 software.

Assignments: 8 homeworks (1 per chapter), 5 quizzes, term paper, midterm/final exams

- 8 homeworks (1 for each chapter) assigned on a (roughly) 1.5 weekly schedule worth 15% of the total grade. Homework assignments are due on the stated due date. No late homework will be accepted under any circumstances.
- However, the lowest HW grade will be dropped in computing the final grade.
- Homework assignments will be a mix of textbook problems and numerical problems based on Matlab or the nanoHUB.org, including code and figures. For such assignments, you will submit all your codes and figures, as will be specified in each HW handout.
- HW assignments will be submitted electronically via Moodle. After the HW deadline, solutions to the HW will be released and you will have an opportunity to “self-grade” your HW and turn in a paper copy along with corrections for an improved grade.
- 5 conceptual lecture-based quizzes given ~every two weeks. Quizzes will be around 15 minutes, given in lecture and will count for 15% of the total grade.
- Term paper will be a 4-page journal-style write-up about a device of your choice, due last day of lecture and worth 20% of the grade.
- Mid-term exam will be given March 7-9 and is worth 20% of the total grade.
- The final exam will be held Wednesday May 9th from 1 to 3 pm. There will be a final review earlier that week (time and location TBD). The final will be cumulative and worth 30% of the total grade.
- Make-up exams will be given only if you provide a valid written excuse and notify me *prior* to the missed exam. Other missed exams will be considered failures.

Course Inclusivity Statement: "The diversity of the participants in this course is a valuable source of ideas, problem solving strategies, and engineering creativity. If you feel that your contribution is not being valued for any reason, please speak with me privately. If you wish to communicate anonymously, you may do so in writing or speak with Dr. Paula Rees, Director of Engineering Diversity Programs (rees@umass.edu, 413.545.6324, Marston 128). We are all

members of an academic community where it is our shared responsibility to cultivate a climate where all students/individuals are valued and where both they and their ideas are treated with respect."

Course Topics:

1. Semiconductor Electronics Fundamentals:
 - a. Crystal structure
 - b. Energy gaps and electronic structure
 - c. Density of states and carrier statistics in equilibrium
 - d. Carrier motion: drift and diffusion
2. Metal-Semiconductor Contacts
3. pn Junctions:
 - a. basic principles and electrostatics
 - b. Generation-recombination processes
 - c. Currents in pn Junctions—ideal diode law
 - d. Non-idealities: junction breakdown mechanisms
4. Select junction devices:
 - a. Heterostructures
 - b. pn junction field effect transistors (JFETs) and MESFETs
5. Bipolar Transistors (BJTs): Basic Properties and Models
6. Properties of the Metal-Oxide-Silicon System
7. MOS Field-Effect Transistors I: Physical Effects and Models
8. MOS Field-Effect Transistors II: High-Field Effects